

JUN-23-2006 P18-20

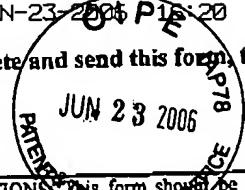
## LATTICE ENGINEERING

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P.01/02

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29416 7590 06/21/2006

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Mark Becker	(Depositor's name)
Mark Becker	(Signature)
6/23/06	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/809,658	03/25/2004	Howard Tang	1054.030	7489

TITLE OF INVENTION: CONFIGURING FPGAS AND THE LIKE USING ONE OR MORE SERIAL MEMORY DEVICES

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$0	\$1400	09/21/2006
EXAMINER	ART UNIT	CLASS-SUBCLASS			
TAN, VIBOL	2819	326-038000	35/26/2006 TBESHAN2 00000073 501958	10889658	

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## 3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Lattice Semiconductor Corporation

Hillsboro, Oregon

Please check the appropriate assignee category or categories (will not be printed on the patent):  Individual  Corporation or other private group entity  Government

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a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.  b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(e)(2).

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Authorized Signature *Mark Becker*Date *6/23/06*Typed or printed name *Mark Becker*Registration No. *31326*

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